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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/064,777
Filing Date	08/16/2002
First Named Inventor	Hsin-Ta Lee
Art Unit	2876
Examiner Name	LABAZE, EDWYN
Attorney Docket Number	CMOP0023USA

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Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 605. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. *Applicant's unique citation designation number (optional). * See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. *Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

NOV 04 2004

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hsin-Ta Lee et al.

5 Examiner: LABAZE, EDWYN

Filing Date: 08/16/2002

Art Unit: 2876

Serial No.: 10/064,777

Docket No.: CMOP0023USA

10

Title: LIQUID CRYSTAL DISPLAY PANEL HAVING REDUCED
FLICKER

To: Commissioner for Patents

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P.O. BOX 1450

Alexandria, VA 22313-1450

Subject: Information disclosure statement under 37C.F.R. §1.56.

20 Dear Sir or Madame:

This is an Information Disclosure Statement in accordance with the duty to disclose information material to patentability under 37 C.F.R. §1.56. The applicant wishes to make of record the document listed on
25 the accompanying form 1449A/PTO. It is respectfully requested that the examiner initials the cited reference on the form and that it be made of record in the application and that a copy of the initialed form be sent to the applicant with the next communication from the examiner.

30 Since this IDS is filed before the mailing date of a final action or notice of allowance, consideration of the information disclosure statement is hereby requested according to 37 CFR §1.97(c). Each item

contained in the IDS was cited in communications from the State Intellectual Property Office of the People's Republic of China on August 6, 2004. The applicant requests that the examiner consider the items contained in this IDS.

5

According to the requirement set forth in 37 C.F.R. §1.98 and M.P.E.P. 609, the applicants are submitting a copy of the cited reference (China Patent CN1287287A) and an English language abstract of an equivalent application (Publication No. JP2001075127A), which was claimed as the priority application of CN1287287A.

The abstract of the application (CN1287287A) is substantially the same as the application (JP2001075127A), thus the English language abstract of the application (JP2001075127A) is hereby presented to fulfill the information disclosure statement requirement set forth in 37 C.F.R. §1.98 and M.P.E.P. 609. The English language abstract of the application (JP2001075127A) is listed as follows:

PROBLEM TO BE SOLVED:

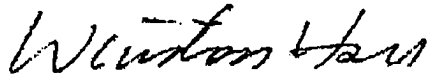
To improve the uniformity of a display screen and to obtain a high picture quality liquid crystal panel by having a parasitic capacitance region section where a pixel output wiring section and a scanning line input section overlap and making the parasitic capacitance have intrasurface distribution.

SOLUTION:

Scanning lines 101 and signal lines 102 are approximately orthogonally cross each other and a TFT element 103 has a parasitic capacitance 106 region section where pixel output wiring section and scanning line input section overlap each other. And, flicker suppressing means are provided in the distribution condition of the capacitors 106 along the scanning line direction to suppress flicker phenomenon of display screen caused by the fact that field through

voltages of active matrix type liquid crystal display elements are different at the input and its opposite sides of scanning line signals of the lines 101. Since the values of the capacitors 106 are not uniform, the variation, in the intrasurface distribution of the field through voltages becomes small, flicker is suppressed and liquid crystal display elements having higher picture quality are obtained. The flicker suppressing means are provided by the specifications of an exposure mask during the production process of the liquid crystal display elements.

Respectfully Submitted,



Date: November 4, 2004

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